

HIGH PERFORMANCE DDR3/Serdes Evaluation PLATFORM



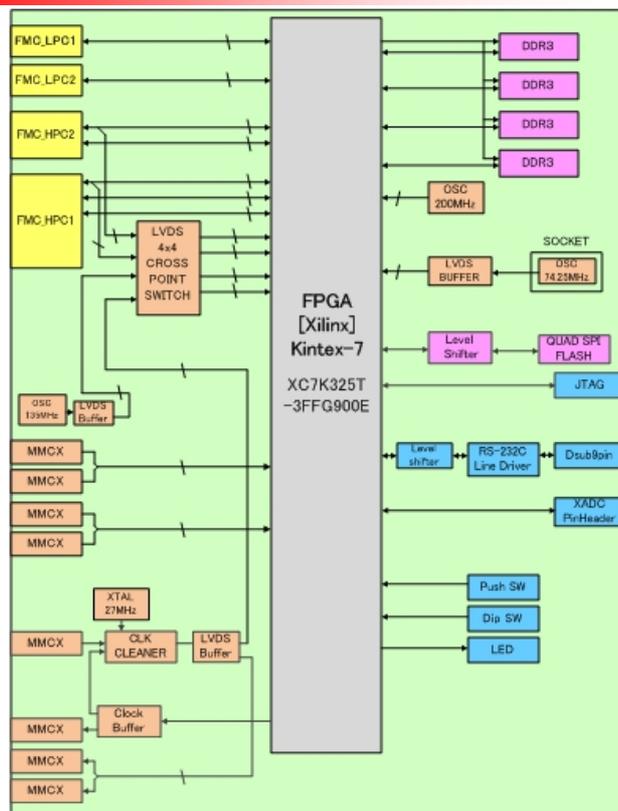
The TB-7K325T-IMG Kintex-7 FPGA Evaluation board allows you to test your 12.5 Gbps serial I/O designs and 64-bit x 1,866Mbps memory interface designs on the world's first 28nm FPGA from Xilinx, the Kintex-7. Realize your solution faster than ever using our reference designs based on industry standard high-bandwidth interfaces (created with FMC cards) that take advantage of the new memory Kintex-7 memory controller innovations. Connectivity, scalability, and flexibility make this board your multi-project, revolutionary innovation platform.



Features

- FPGA : Kintex ®-7 XC7K325T-3FFG900E
- Memory : DDR3 2Gbit x4
- Configuration : QUAD SPI Flash (128Mbit)
- FMC connector
 - ❖ HPC (High Pin Count) x2
 - ❖ LPC (Low Pin count) x2
- Transceiver : 16ch x 12.5Gbps (GTX)
- Clocking
 - ❖ 74.25MHz (via socket)
 - ❖ 135MHz OSC
 - ❖ 200MHz OSC
 - ❖ PLL (user programmable)
- MMCX for Input and Output
- XADC Pin headers

Block Diagram



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